

PATENT

Atty. Docket No. 8055-83 CIP (In1292USD1)

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT(S): Gerald Friese Examiner: William A. Powell
SERIAL NO.: 09/893,160 Group Art Unit: 1765
FILED: June 27, 2001
FOR: POWER PADS FOR APPLICATION OF HIGH CURRENT
PER BOND PAD IN SILICON TECHNOLOGY

Dated: June 10, 2003

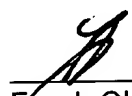
Mail Stop Issue Fee
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

TRANSMITTAL OF FORMAL DRAWINGS

Sir:

Applicant submits herewith six (6) sheets of formal drawings
depicting FIGS. 1-7 for this application.

Respectfully submitted,



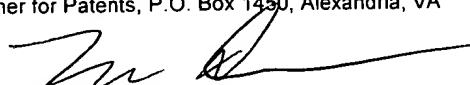
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Frank V. DeRosa

03-03



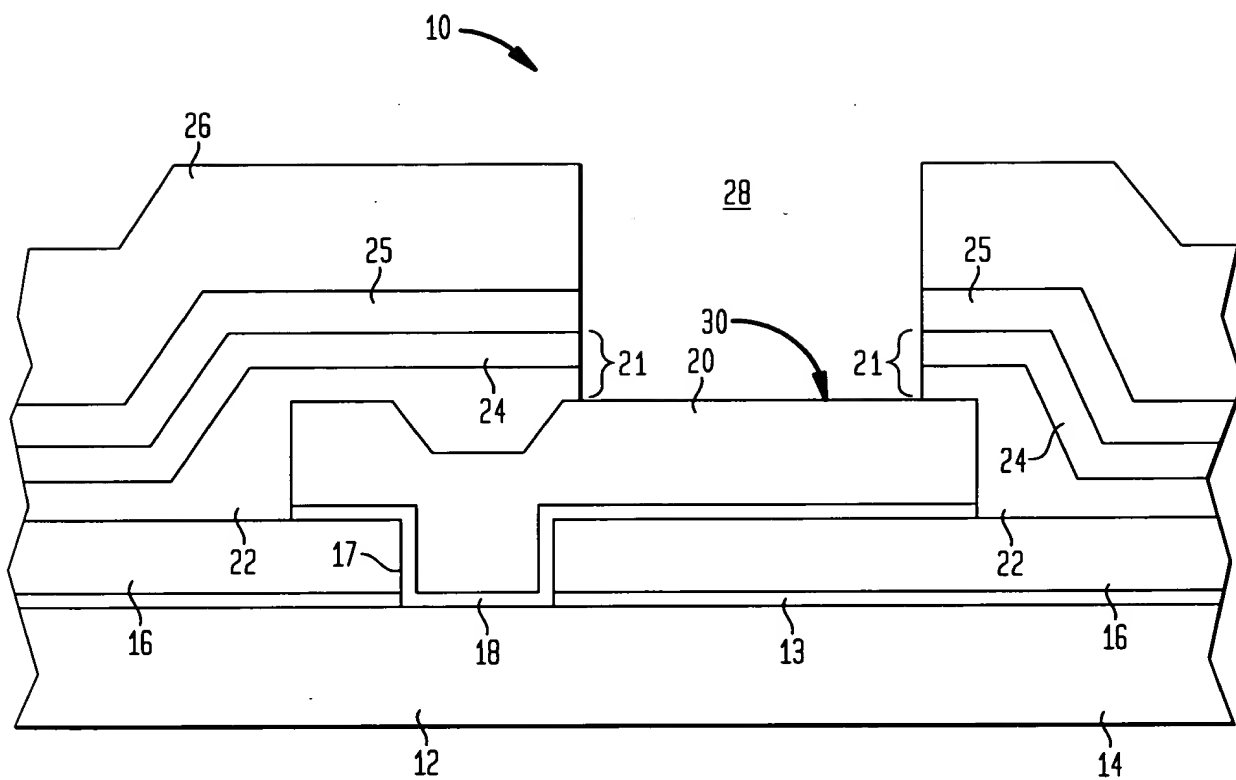
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FIG. 1





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FIG. 2

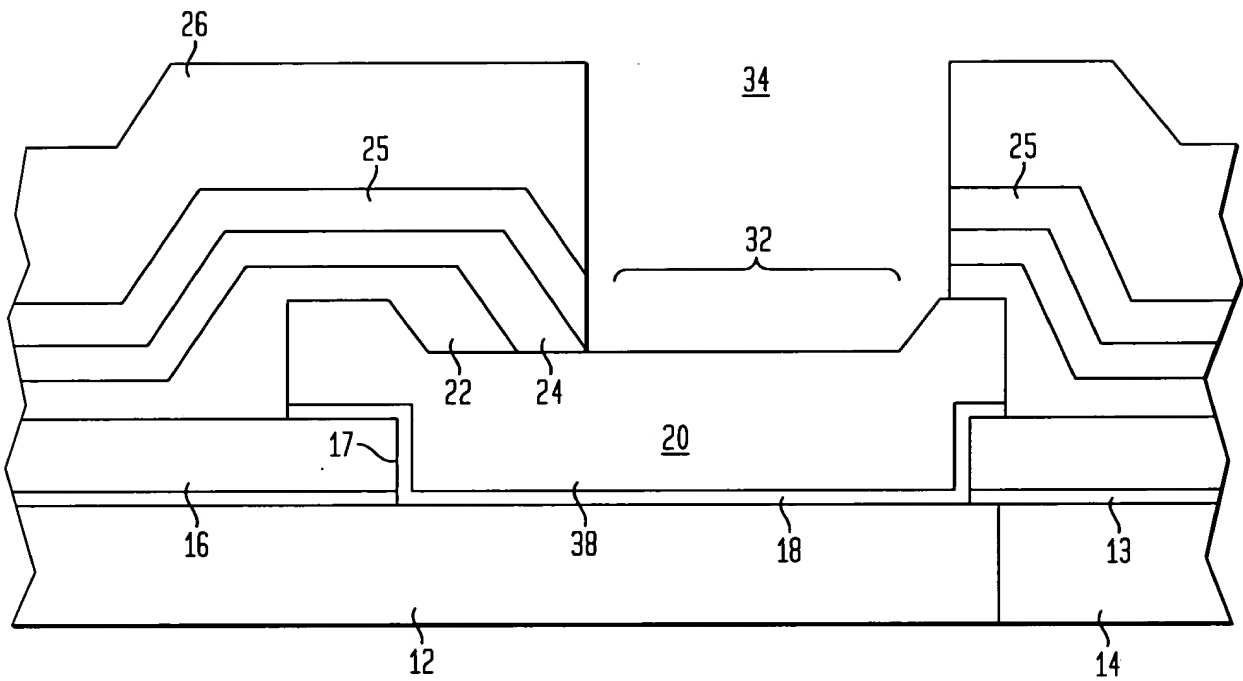


FIG. 3

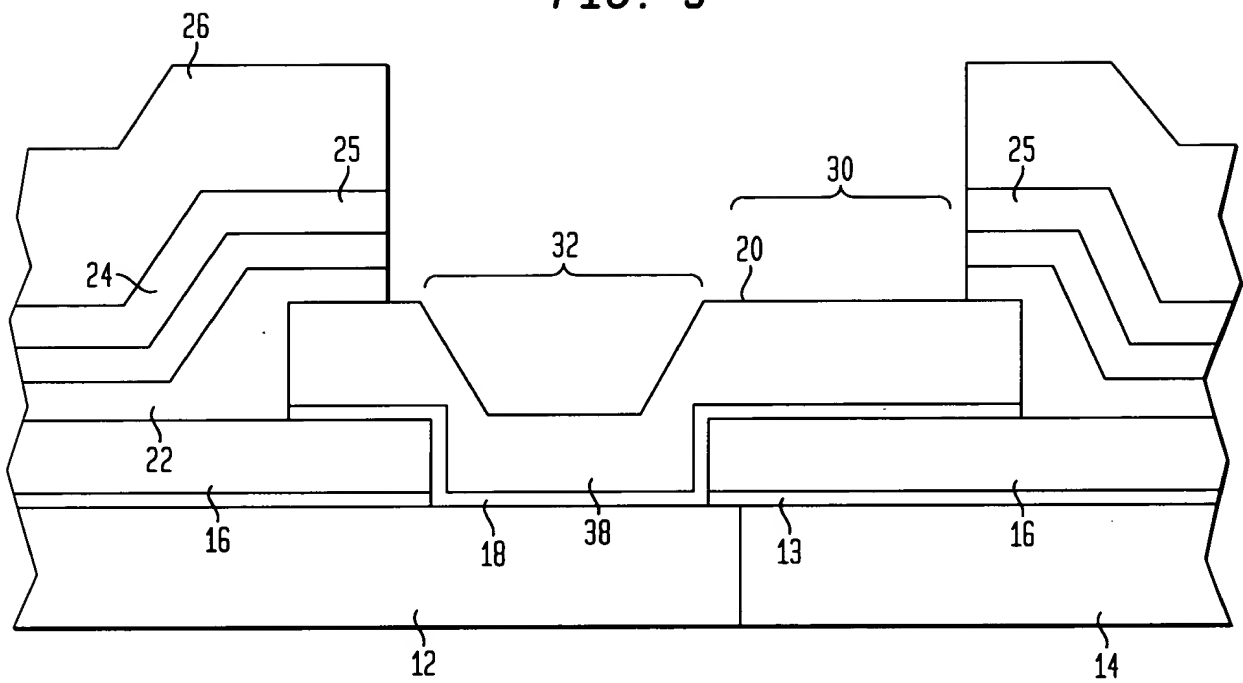


FIG. 4

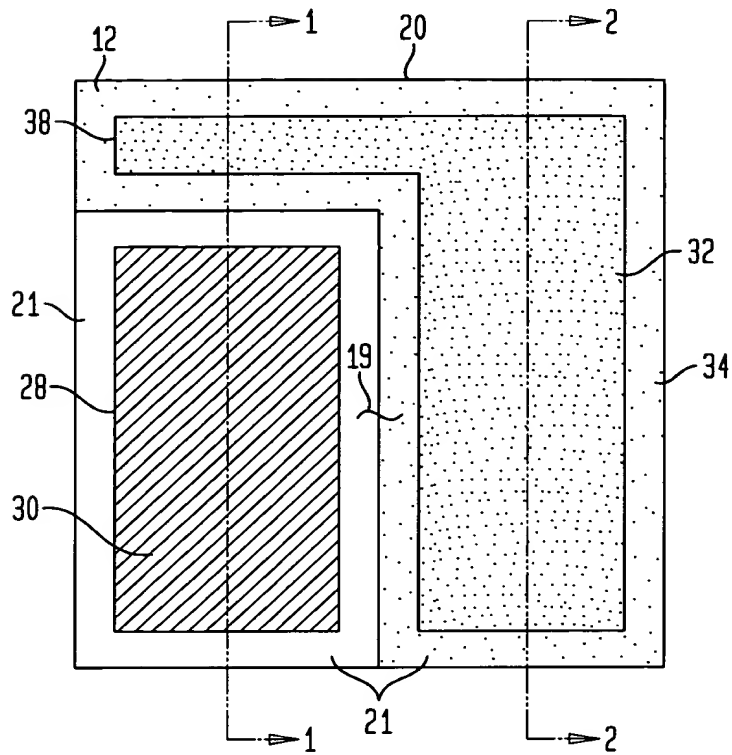


FIG. 5

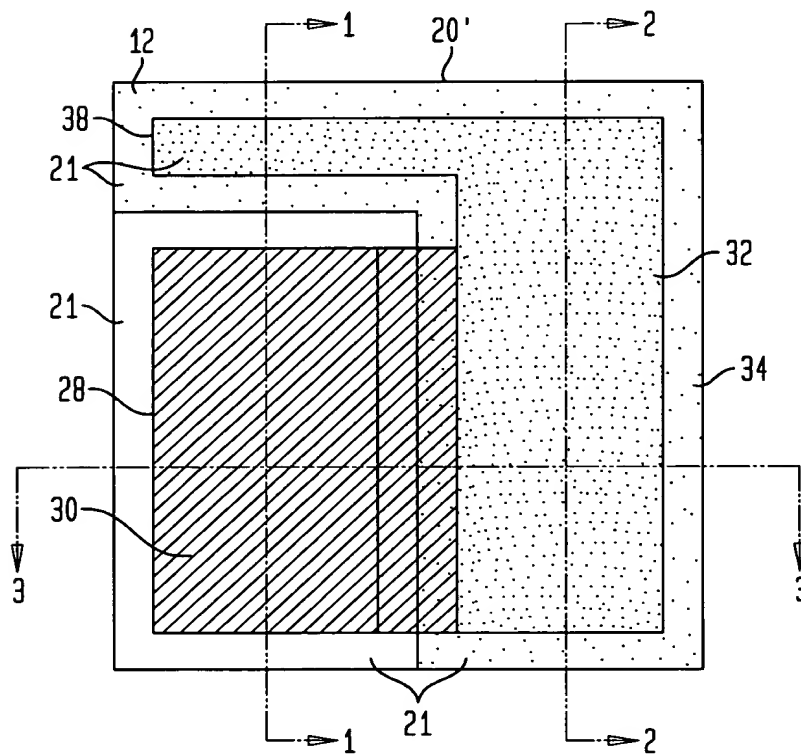
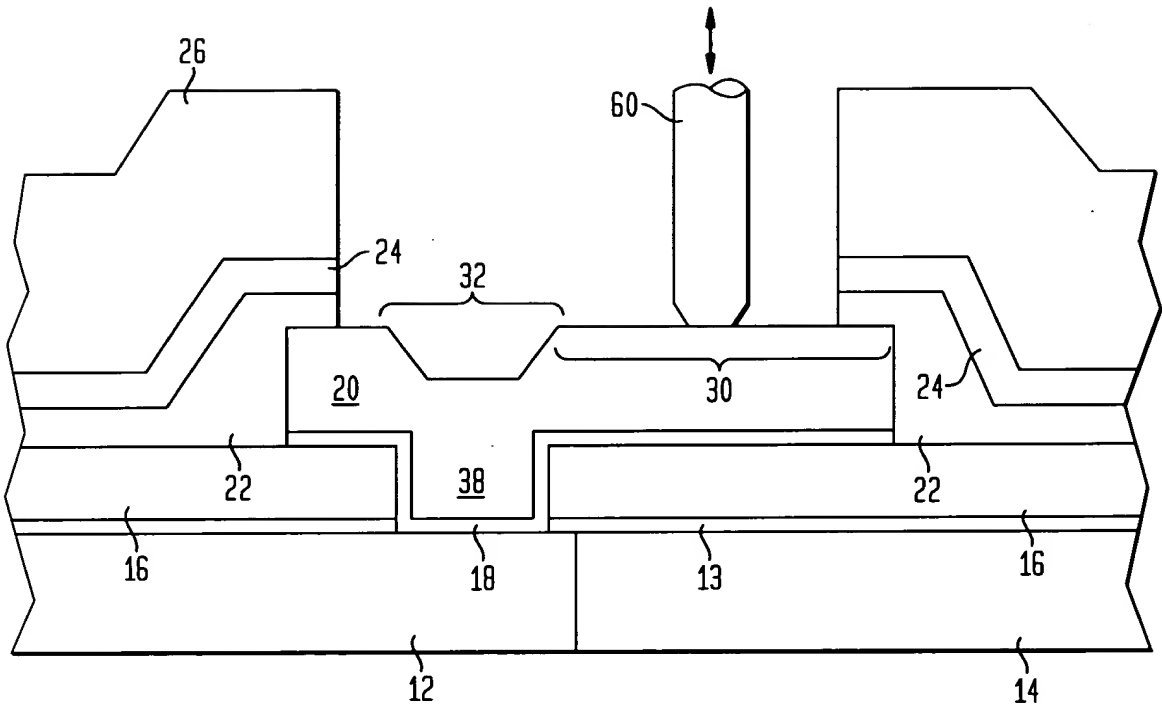


FIG. 6





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FIG. 7

